Week 9 Critique

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1. [Traffic management: a holistic approach to memory placement on NUMA systems](https://dl.acm.org/citation.cfm?id=2451157)

This paper provides new sight to bottleneck of modern NUMA system. In traditional view accessing remote memory is too expensive, previous architecture tends to data in local memory. However, thanks to the improvement of hardware, difference between time to accessing local and remote memory is a lot smaller than previous one. Therefore, this paper suggests that main bottleneck on modern NUMA system is not accessing remote memory, but traffic congestion on memory controllers and interconnects. This paper is much impressive than other paper to me, because I’m doing research on new hardware called Non-Volitile RAM. from now on, I will pay more attention on hardware because it can affect software design.